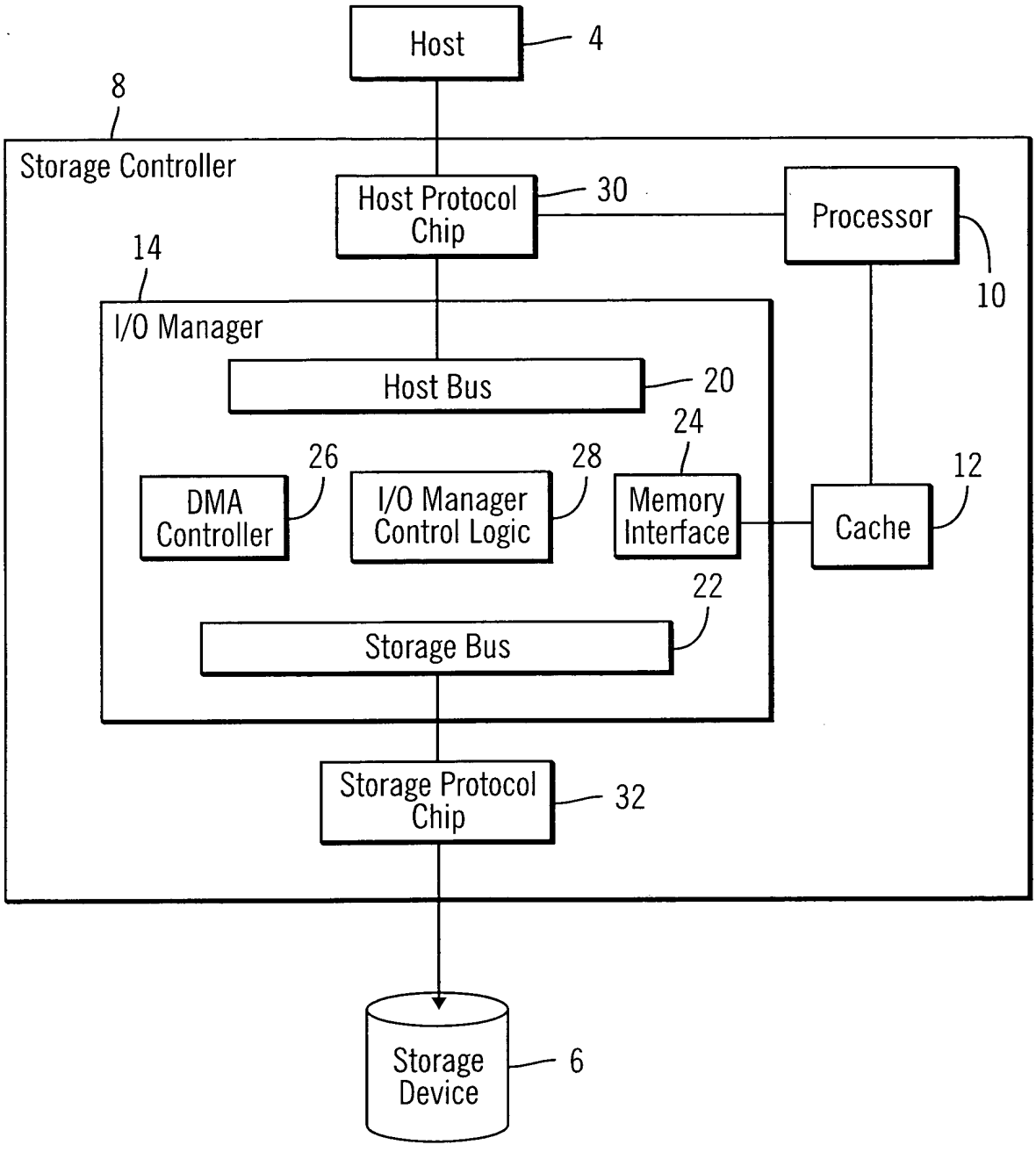

FIG. 1

50

Hardware Control Block	
Byte	Field
0-5	Target PA
0 - 1	Disk ID
2 - 5	LBA
6-7	Reserved for processor.
8-23	Reserved for I/O Manager.
24-25	Controls
Bits 0 - 9	Reserved
Bit 10	Recalculate LRC after Target PA is added and add LRC.
Bit 11	Add target PA; use the target PA. The LBA is used for the first transferred sector. Increment the LBA for subsequent transferred sector.
Bit 12	Check LRC.
Bit 13	Check PA. Use the source PA. LBA must match first transferred sector. Increment the LBA for subsequent transferred sector.
Bits 14 - 15	Address Conversion: 00 - 512 cache to 512 host. 01 - 512 host to 520 cache. 10 - 520 cache to 512 disk. 11 - 520 cache/disk.
26-31	Source PA
26-27	Disk ID
28-31	LBA

FIG. 2

60

ADDRESS FORMAT	
Bit Offset	Field Description
63	Reserved
62	Hardware Control Block enabled (0 - do not use HCB; 1 - use HCB index).
61-64	Hardware Control Block Index.
43-33	Reserved
32-0	Memory Address

FIG. 3

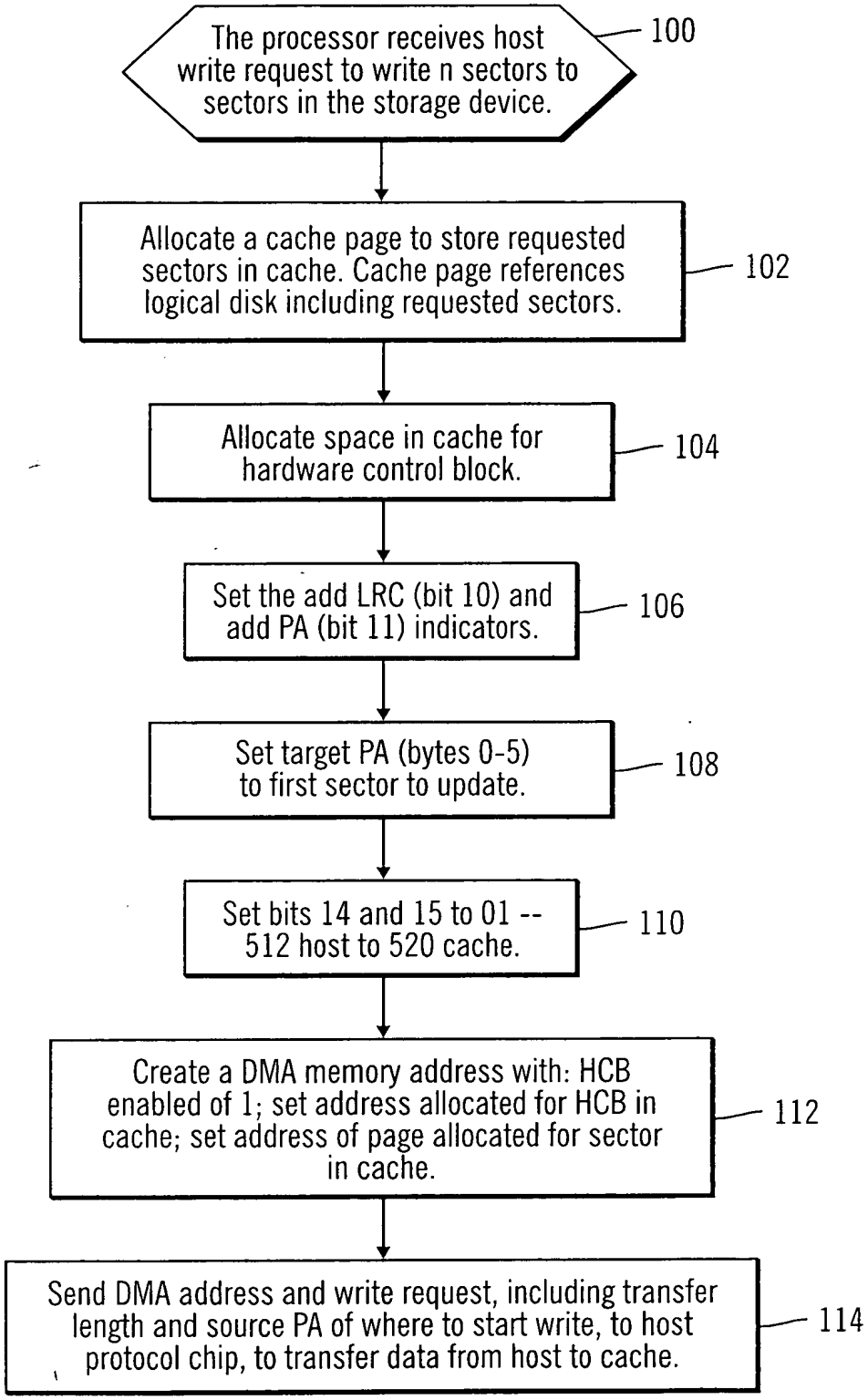

FIG. 4

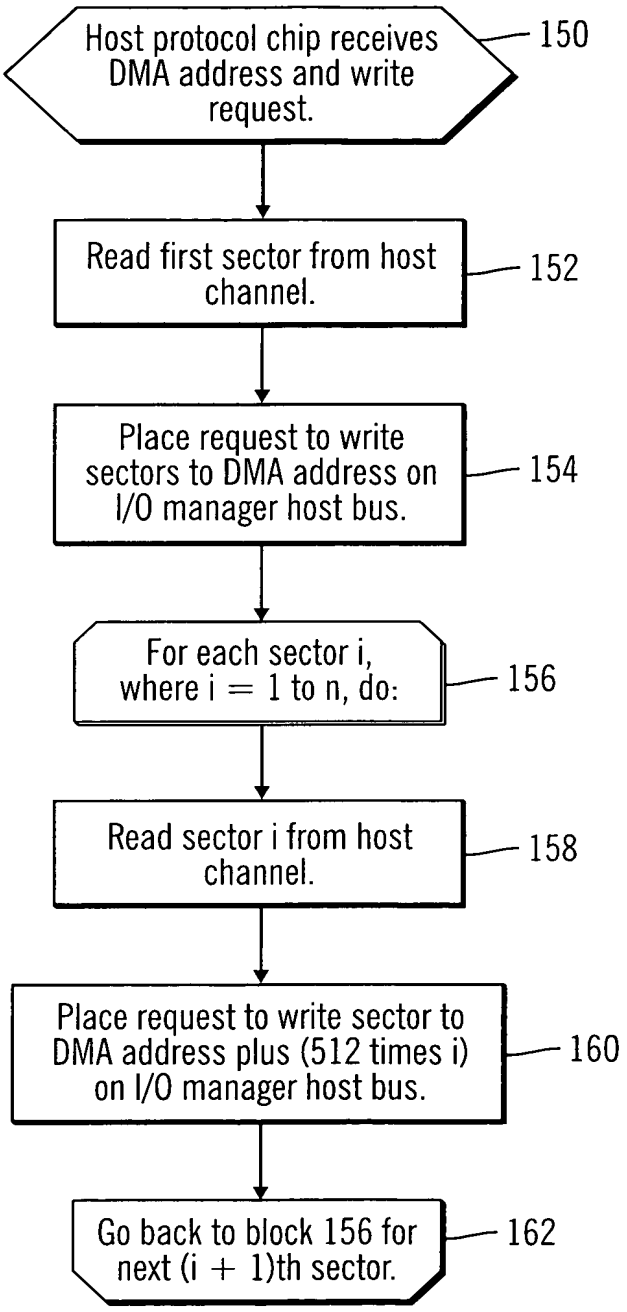

FIG. 5

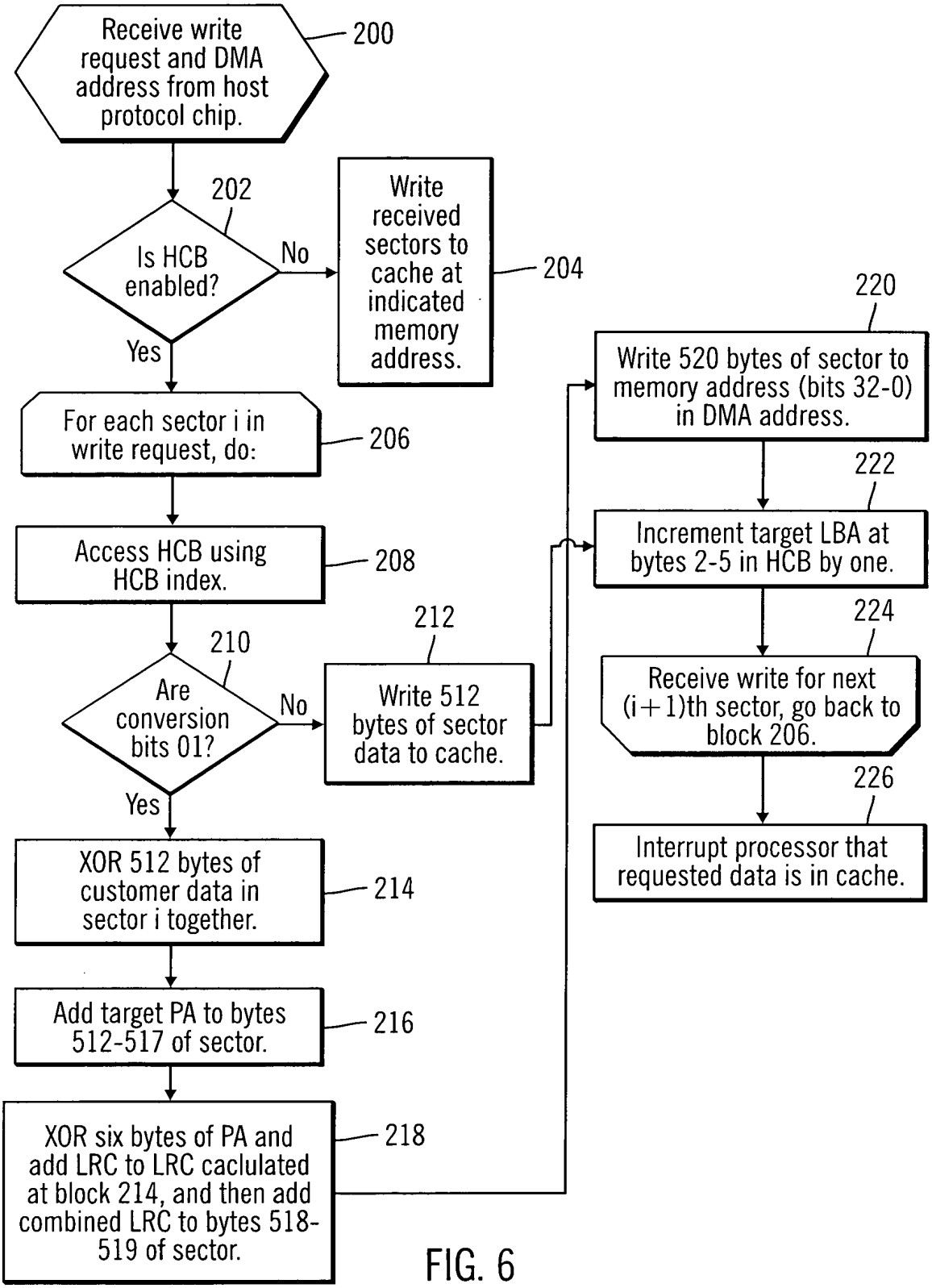

FIG. 6

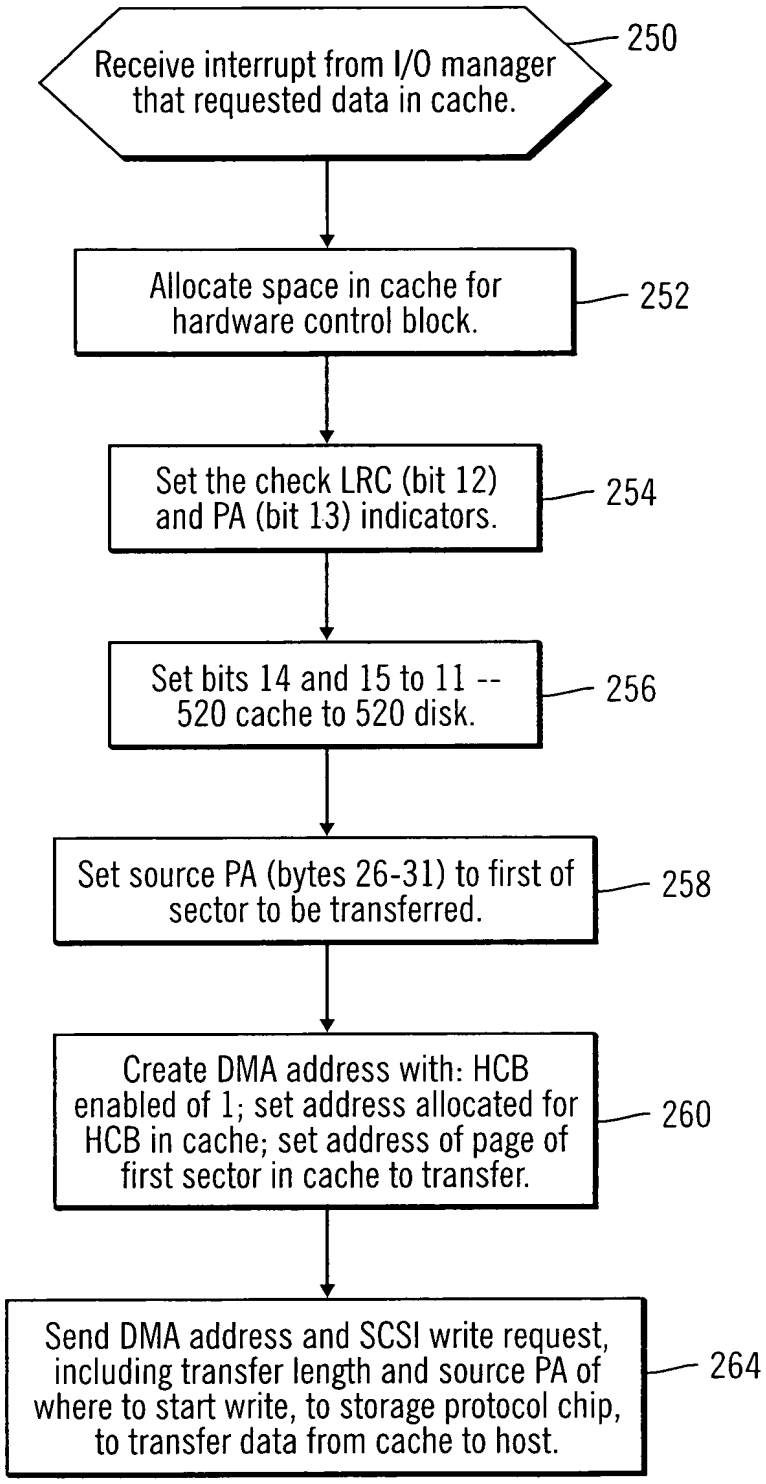

FIG. 7

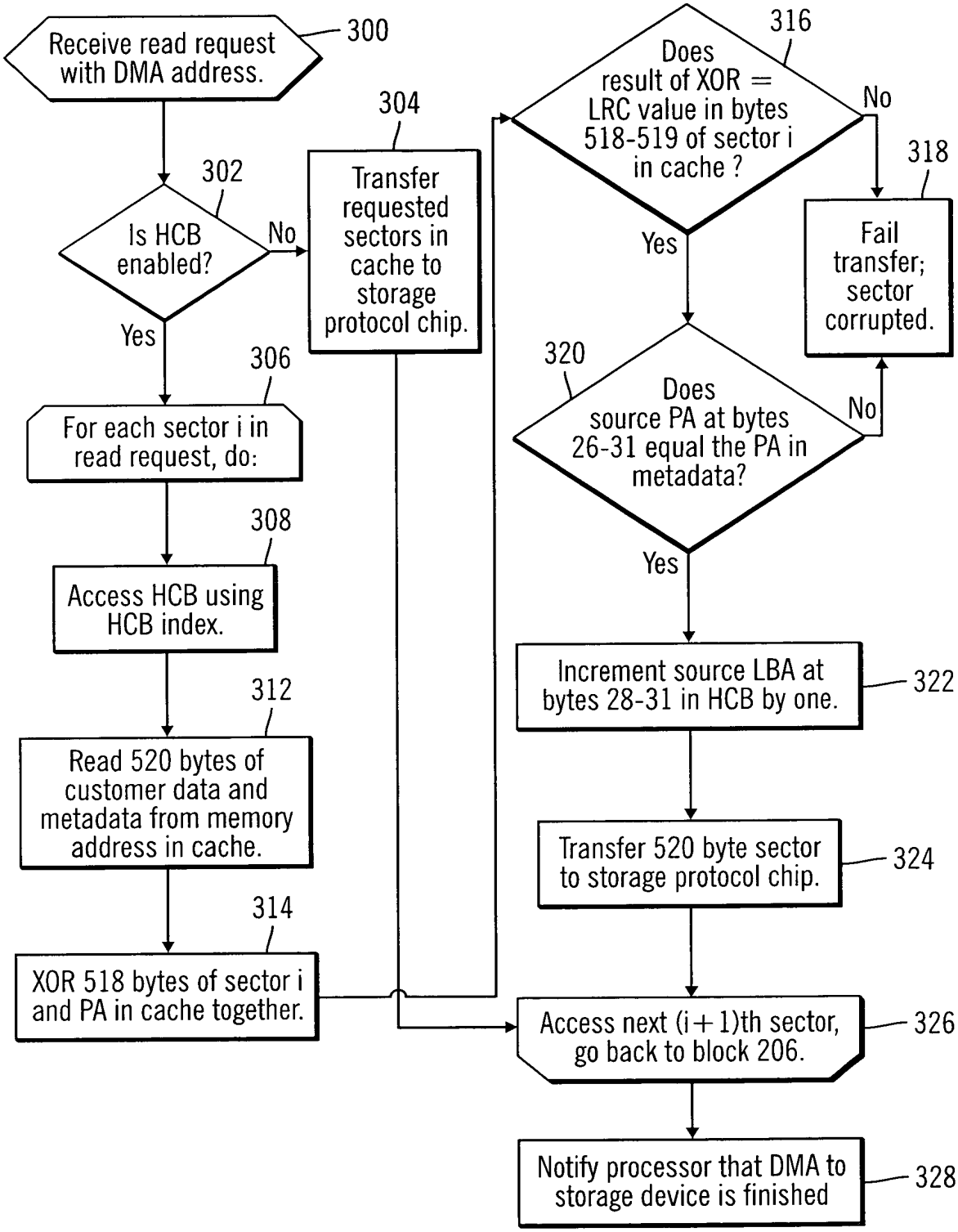

FIG. 8

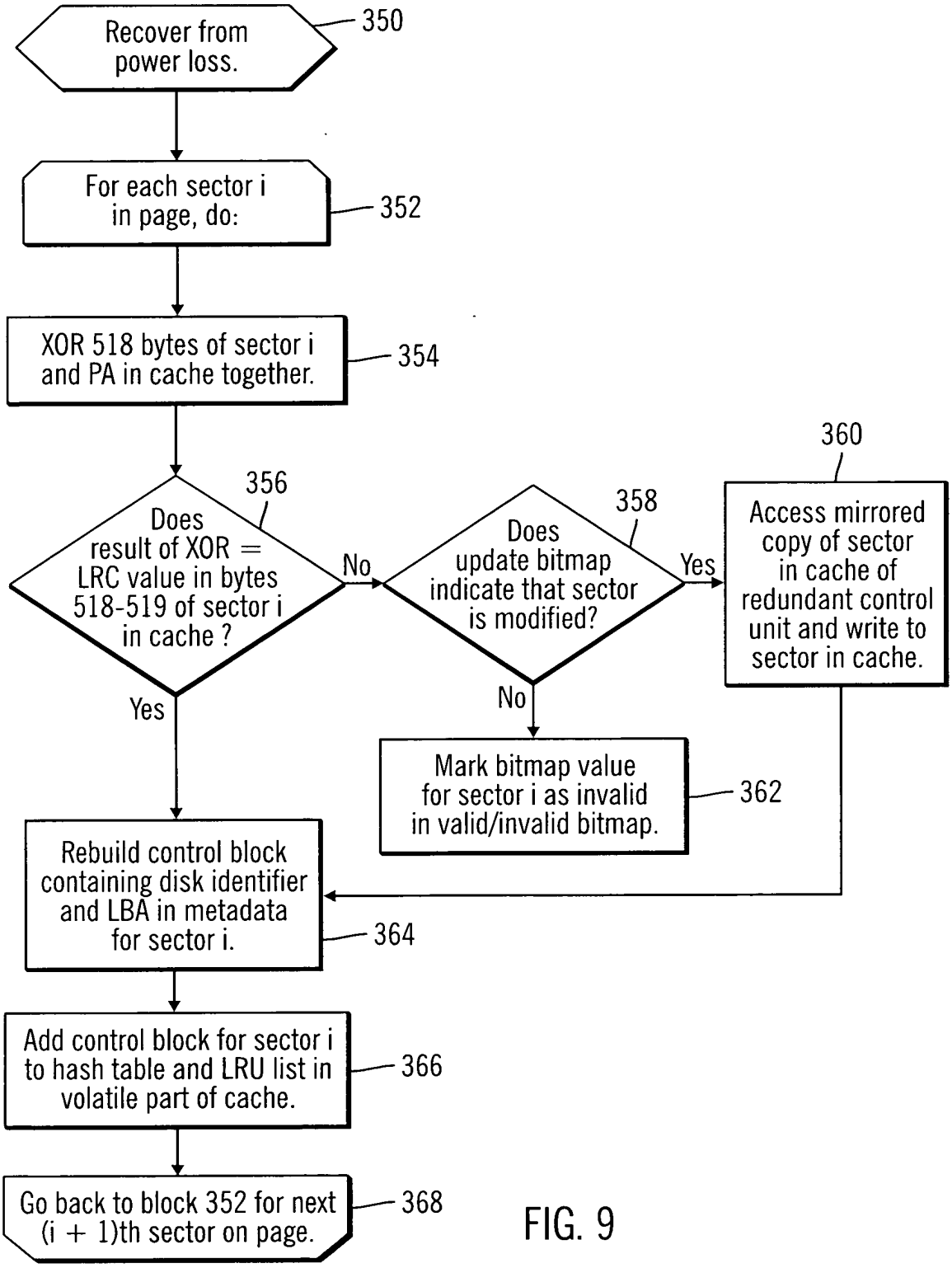

FIG. 9